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   Kind, A.; Padget, J.;
   Parallel and Distributed Processing, 1998. PDP '98. Proceedings of the Sixth Euromicro Workshop on
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   IEEE CNF

2. **Interactive multi-user multimedia environments on the Internet: an overview of DAMSEL and its implementation**
   Pazandak, P.; Srivastava, J.;
   Multimedia Computing and Systems, 1996., Proceedings of the Third IEEE International Conference on
   17-23 June 1996 Page(s):287 - 290
   IEEE CNF

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   Zeller, H.;
   Compcon Spring '90. 'Intellectual Leverage'. Digest of Papers. Thirty-Fifth IEEE Computer Society International Conference.
   26 Feb.-2 March 1990 Page(s):484 - 487
   IEEE CNF

4. **Multithreaded languages for scientific and technical computing**
   Pancake, C.M.;
   Proceedings of the IEEE
   Volume 81, Issue 2, Feb. 1993 Page(s):288 - 304
   IEEE JNL

5. **Logic specifications for multiple robots based on a current programming language**
   Nishiyama, H.; Ohwada, H.; Mizoguchi, F.;
   Intelligent Robots and Systems, 1998. Proceedings., 1998 IEEE/RSJ International Conference on
   Volume 1, 13-17 Oct. 1998 Page(s):286 - 291 vol.1
   IEEE CNF

6. **Datarol: a massively parallel architecture for functional languages**
   Amamiya, M.; Taniguchi, R.;
   Parallel and Distributed Processing, 1990. Proceedings of the Second IEEE Symposium on
   9-13 Dec. 1990 Page(s):726 - 735
   IEEE CNF

7. **Implementation of an object-oriented functional language on the multithreaded architecture**
   Chang Juno; Song Jonghoon; Kim Junghwan; Han Sangyong; Kim Heunghwan;
   Parallel and Distributed Systems, 1997. Proceedings., 1997 International Conference on
   10-13 Dec. 1997 Page(s):294 - 301
   IEEE CNF

8. **Narada: an environment for specification and execution of multi-system applications**
   Halabi, Y.; Ansari, M.; Batra, R.; Jin, W.; Karabatis, G.; Krychiniak, P.; Rusinkiewicz, M.; Suardi, L.;
   Systems Integration, 1992. ICSI '92., Proceedings of the Second International Conference on
   15-18 June 1992 Page(s):680 - 690
   IEEE CNF

9. **Data-parallel programming on multicomputers**
   Quinn, M.J.; Hatcher, P.J.;

Software, IEEE
Volume 7, Issue 5, Sept. 1990 Page(s):69 - 76
IEEE JNL

10. **Execution mode management and CU/PE overlap in an SIMD/SPMD parallel language/compiler**
Nichols, M.A.; Siegel, H.J.; Dietz, H.G.;
Computer Software and Applications Conference, 1991. COMPSAC '91., Proceedings of the Fifteenth Annual
International
11-13 Sept. 1991 Page(s):392 - 397
IEEE CNF

11. **Executing multidatabase transactions**
Ansari, M.; Rusinkiewicz, M.; Ness, L.; Sheth, A.;
System Sciences, 1992. Proceedings of the Twenty-Fifth Hawaii International Conference on
Volume ii, 7-10 Jan. 1992 Page(s):335 - 346 vol.2
IEEE CNF

12. **A shared-memory execution model for parallel functional programming**
Lee, C.C.; Fatmi, H.A.;
UK IT 1990 Conference
19-22 Mar 1990 Page(s):247 - 250
IEE CNF

13. **Future directions in programming languages**
DiNitto, S.A., Jr.;
Computer Languages, 1988. Proceedings., International Conference on
9-13 Oct. 1988 Page(s):169 - 176
IEEE CNF

14. **An implementation of a high-level language for a data-driven processor**
Yamasaki, T.; Munakata, K.; Shima, K.; Yoshida, S.; Terada, H.;
Signals, Systems and Computers, 1991. 1991 Conference Record of the Twenty-Fifth Asilomar Conference on
4-6 Nov. 1991 Page(s):79 - 84 vol.1
IEEE CNF

15. **An executor for multidatabase transactions which achieves maximal parallelism**
Cameron, E.J.; Ness, L.A.; Sheth, A.P.;
Interoperability in Multidatabase Systems, 1991. IMS '91. Proceedings., First International Workshop on
7-9 April 1991 Page(s):288 - 291
IEEE CNF

16. **IPL: a multidatabase transaction specification language**
Jiansan Chen; Bukhres, O.; Elmagarmid, A.K.;
Distributed Computing Systems, 1993., Proceedings the 13th International Conference on
25-28 May 1993 Page(s):439 - 448
IEEE CNF

17. **Trace-driven and program-driven simulation: a comparison**
Malloy, B.A.;
Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 1994., MASCOTS '94.,
Proceedings of the Second International Workshop on
31 Jan.-2 Feb. 1994 Page(s):395 - 396
IEEE CNF

18. **Experiences in simulating multi-agent systems using TÆMS**
Vincent, R.; Horling, B.; Lesser, V.;
MultiAgent Systems, 2000. Proceedings. Fourth International Conference on
10-12 July 2000 Page(s):455 - 456
IEEE CNF

19. **A multiagent robot language for communication and concurrency control**

Nishiyama, H.; Ohwasa, H.; Mizoguchi, F.;
Multi Agent Systems, 1998. Proceedings. International Conference on
3-7 July 1998 Page(s):206 - 213
IEEE CNF

20. **Microprocessor specification in Hawk**
Matthews, J.; Cook, B.; Launchbury, J.;
Computer Languages, 1998. Proceedings. 1998 International Conference on
14-16 May 1998 Page(s):90 - 101
IEEE CNF

21. **A parallel execution method of production systems with multiple worlds**
Matsuzawa, K.;
Tools for Artificial Intelligence, 1989. Architectures, Languages and Algorithms. IEEE International Workshop on
23-25 Oct. 1989 Page(s):339 - 344
IEEE CNF

22. **Execution of extended multidatabase SQL**
Suardi, L.; Rusinkiewicz, M.; Litwin, W.;
Data Engineering, 1993. Proceedings. Ninth International Conference on
19-23 April 1993 Page(s):641 - 650
IEEE CNF

23. **Supporting temporal multimedia operations in object-oriented database systems**
Aberer, K.; Klas, W.;
Multimedia Computing and Systems, 1994., Proceedings of the International Conference on
15-19 May 1994 Page(s):352 - 361
IEEE CNF

24. **Experience with executing shared memory programs using fine-grain communication and multithreading in EM-4**
Sato, M.; Kodama, Y.; Sakai, S.; Yamaguchi, Y.;
Parallel Processing Symposium, 1994. Proceedings., Eighth International
26-29 April 1994 Page(s):630 - 636
IEEE CNF

25. **On the nature and inadequacies of transport timing delay constructs in VHDL descriptions**
Walker, P.A.; Ghosh, S.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 16, Issue 8, Aug. 1997 Page(s):894 - 915
IEEE JNL